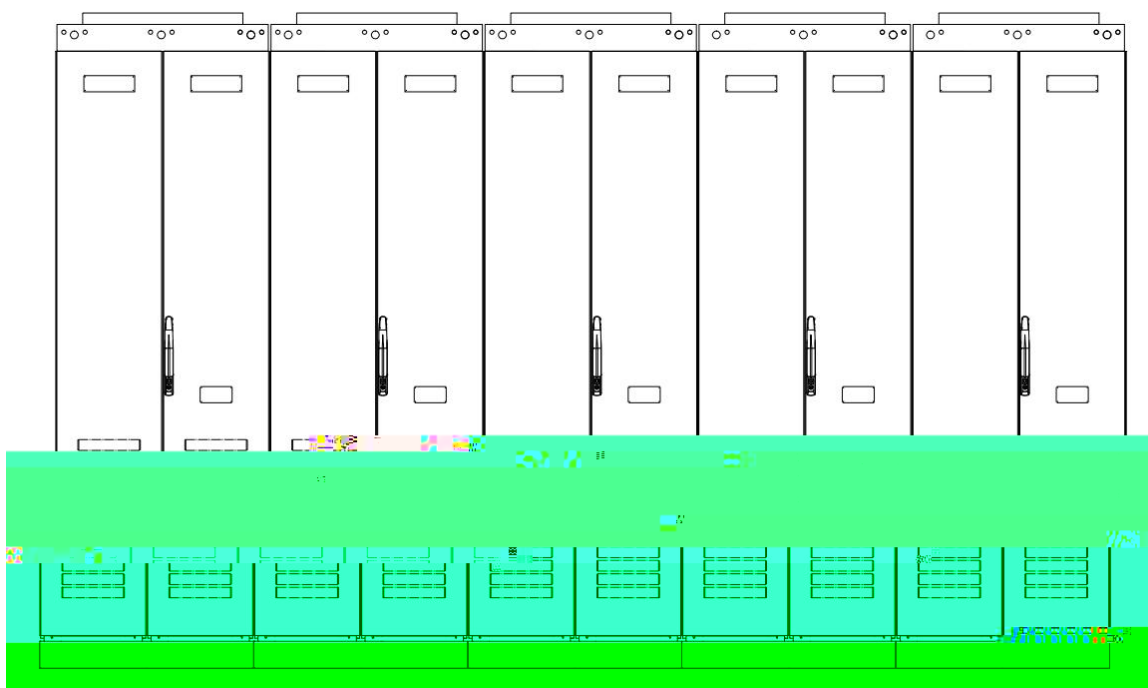
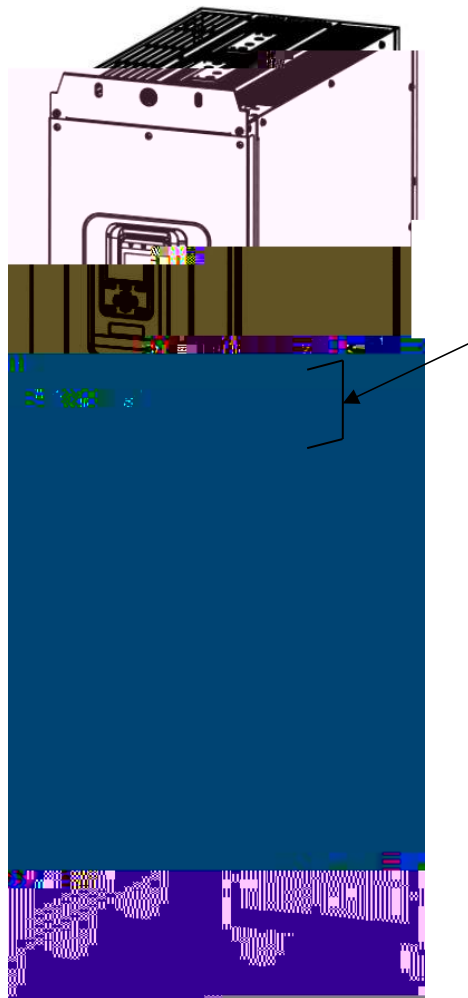
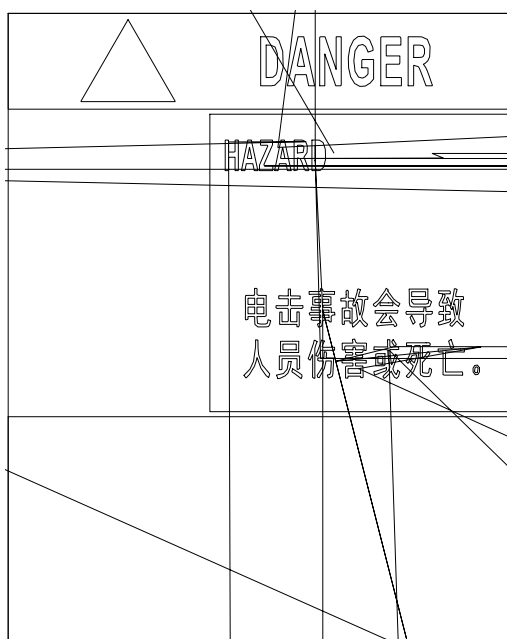
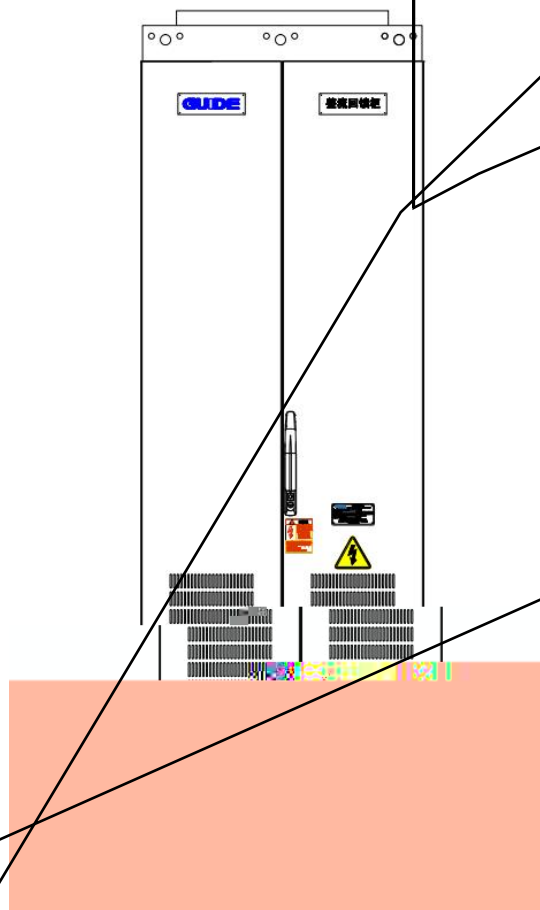


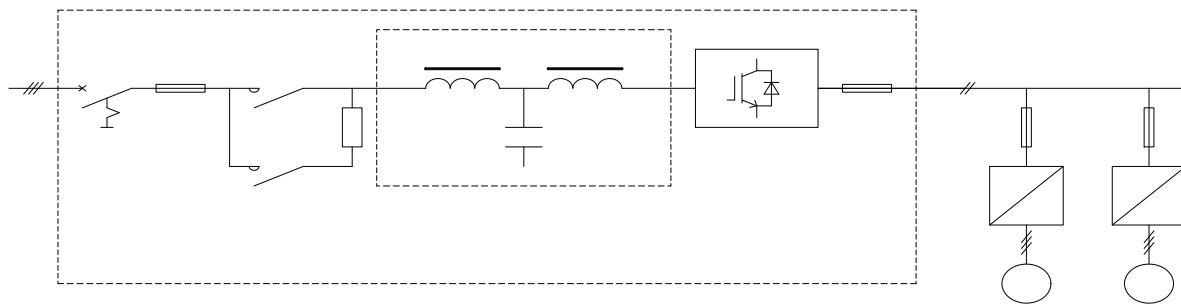
GUIDE

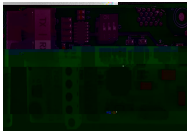
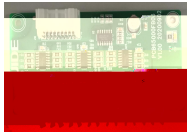
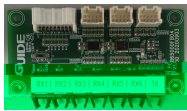
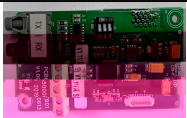




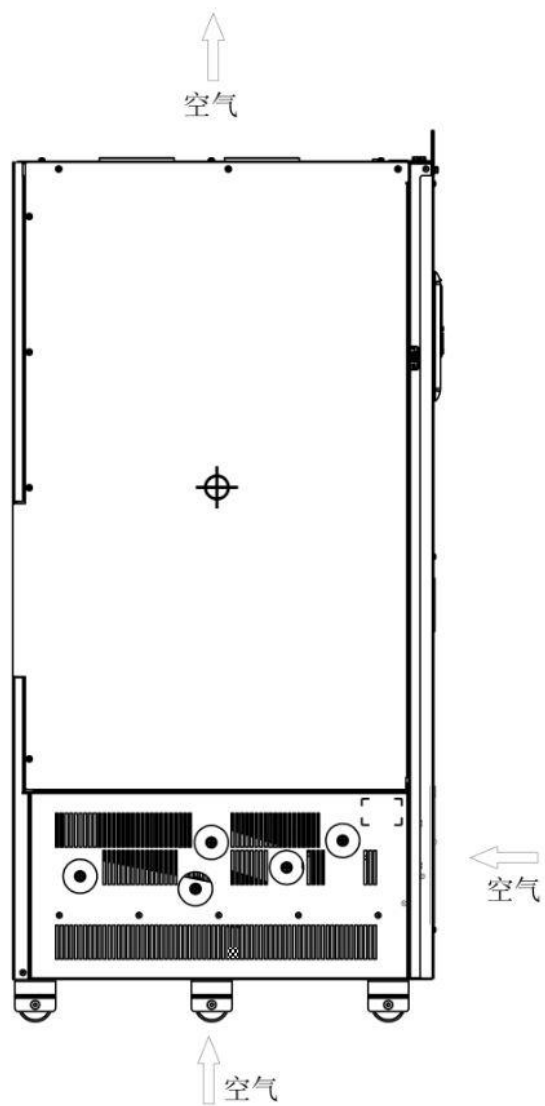
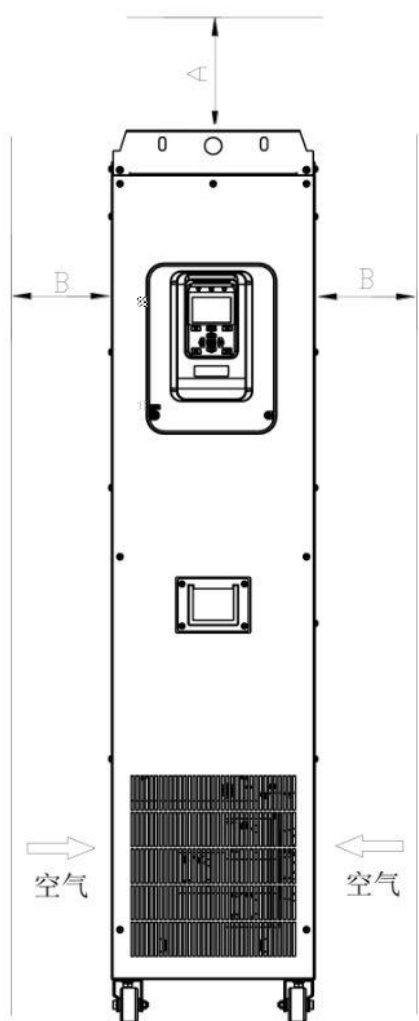






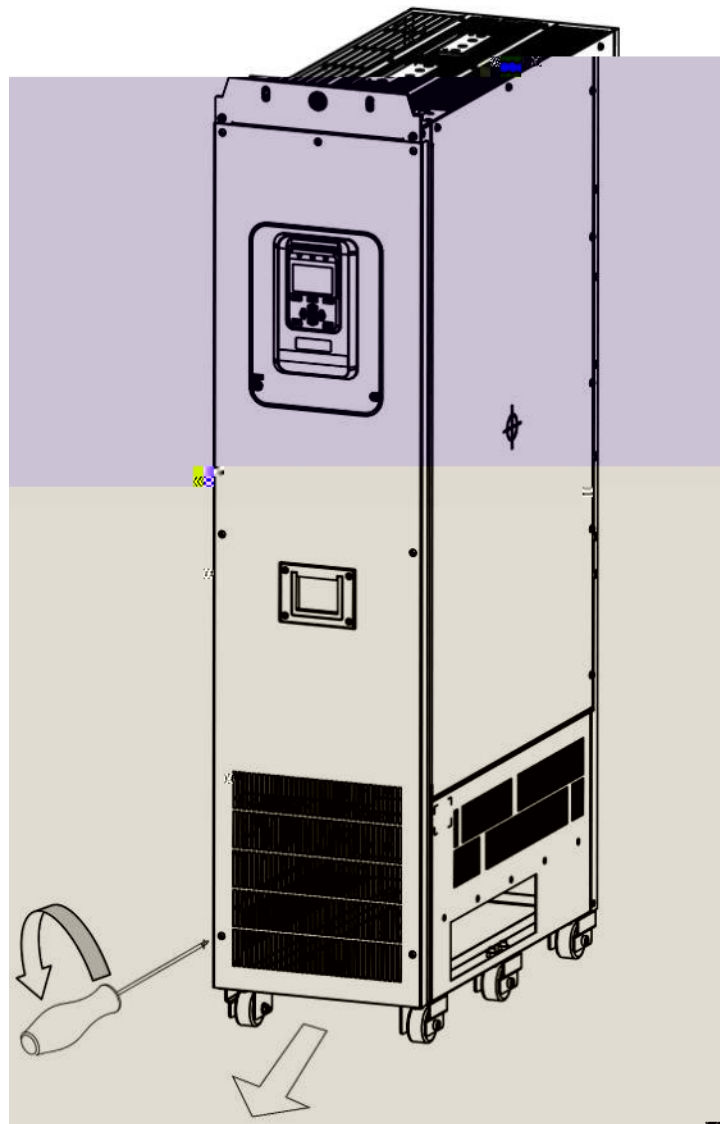
		
		
		
		
		
		
		

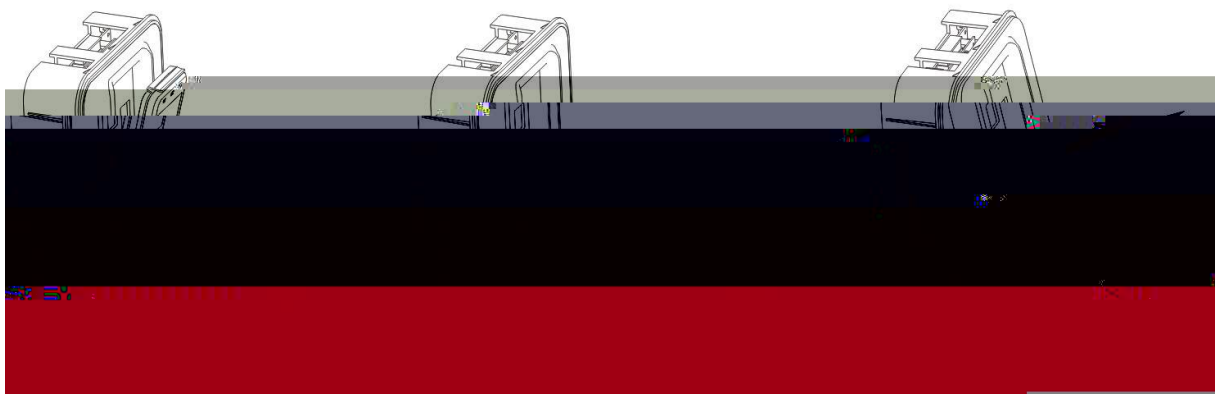


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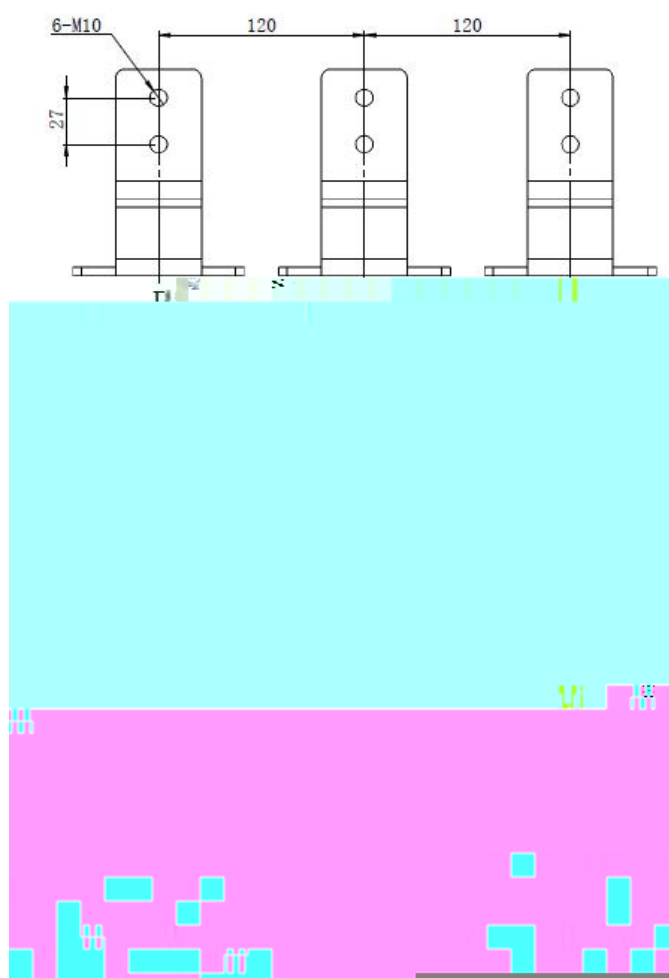
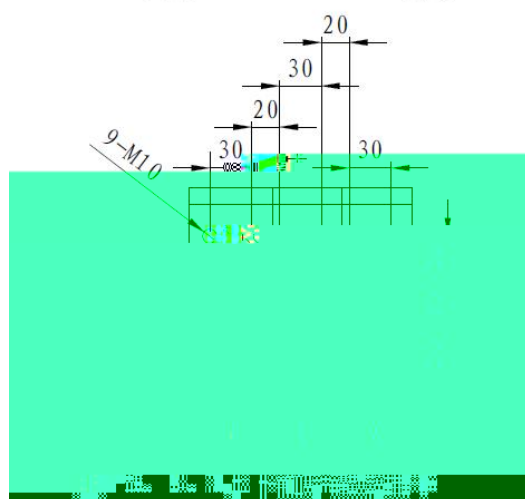
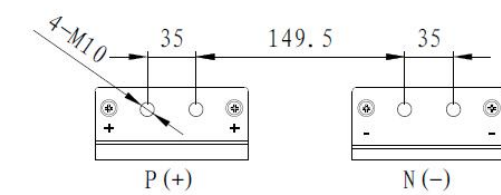


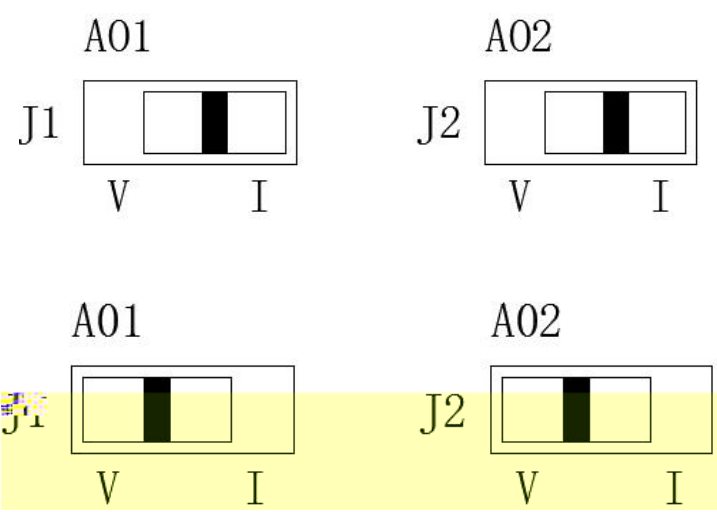


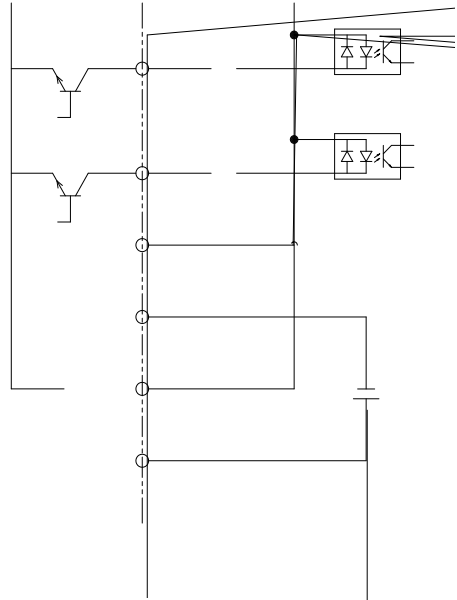
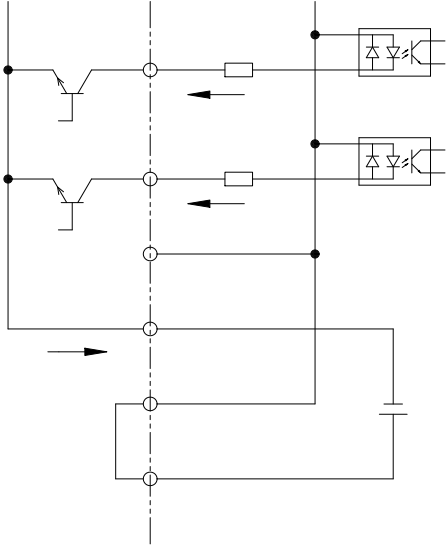
1.

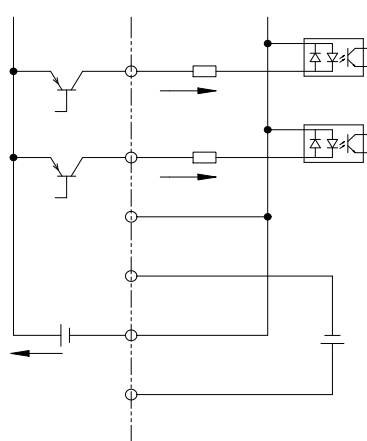
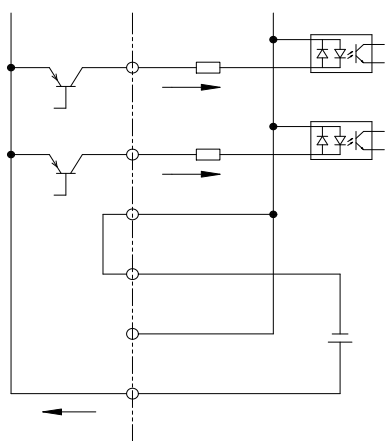
2.

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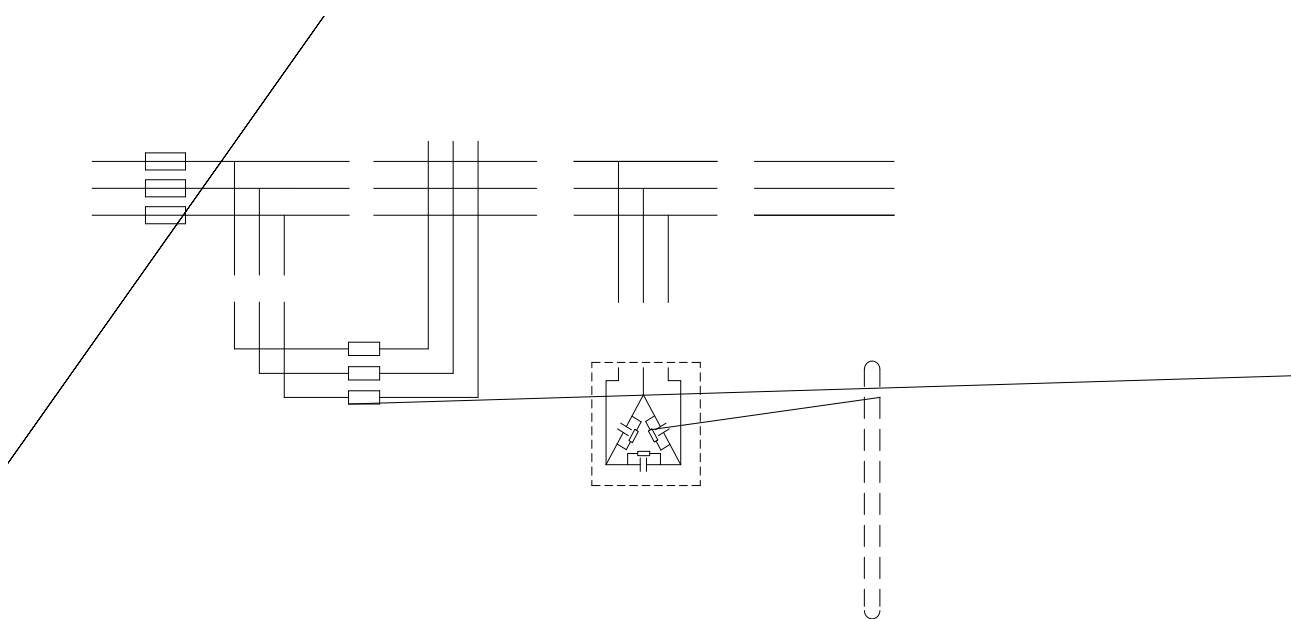


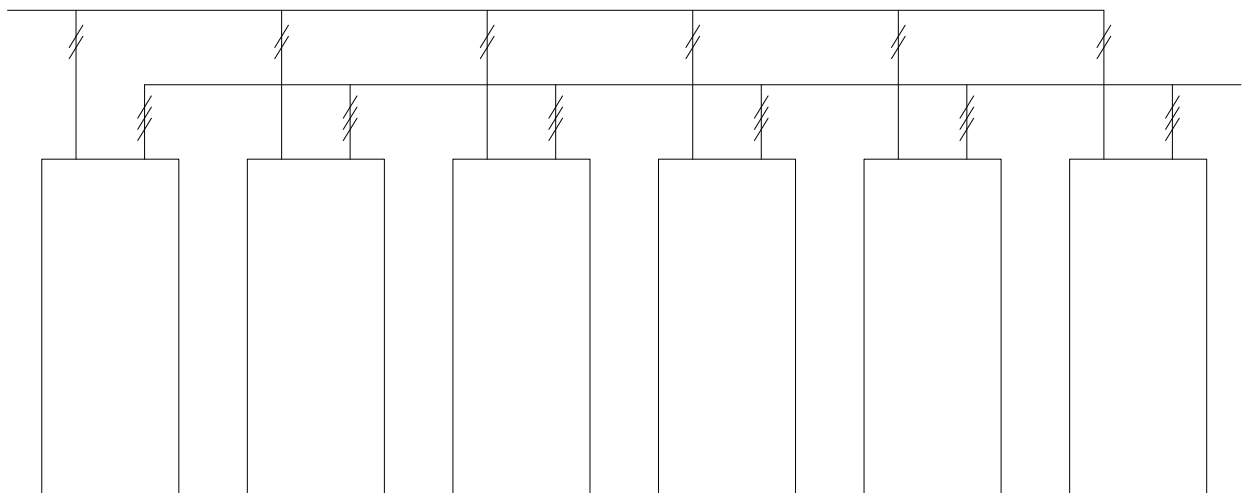


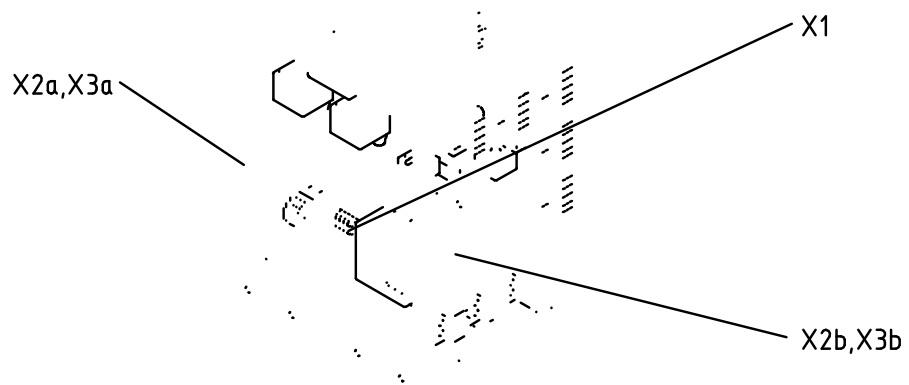




The diagram shows five empty tables arranged horizontally. Each table has a light blue header row and a light gray body. The first table has 2 columns and a header with a single horizontal line above it. The second table has 1 column and a header with three small squares above it. The third table has 1 column and a header with a single horizontal line above it. The fourth table has 1 column and a header with two small squares above it. The fifth table has 1 column and a header with two small squares above it.







P

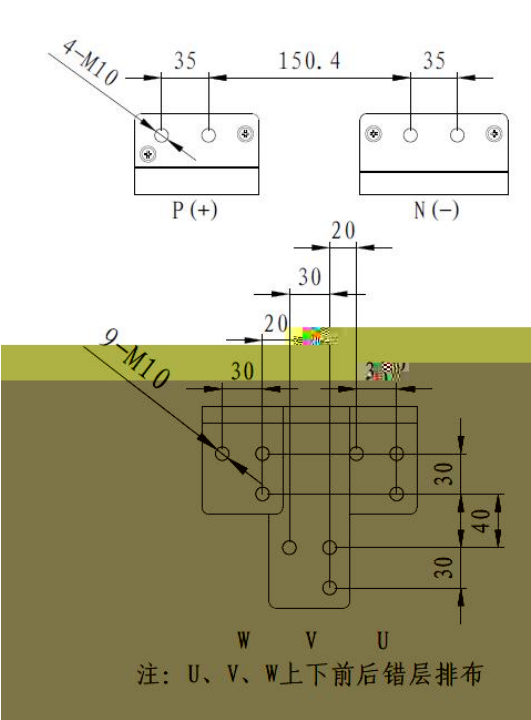
N

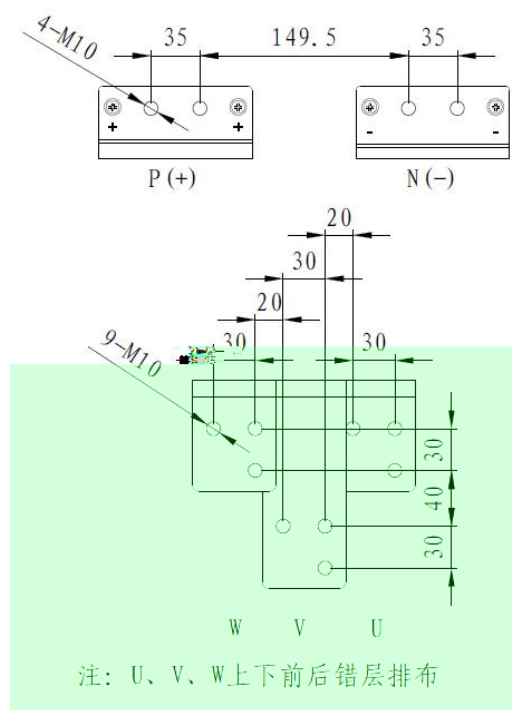
U

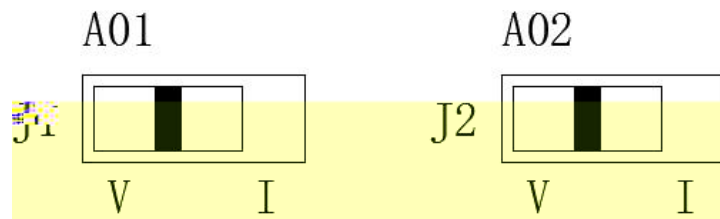
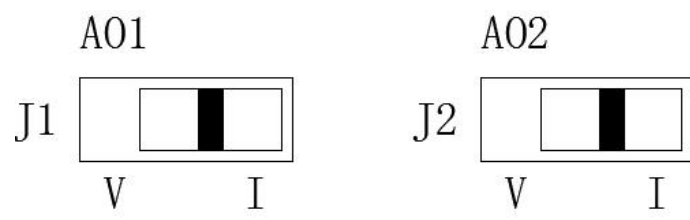
V

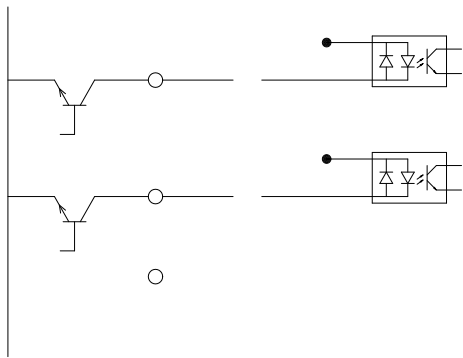
W

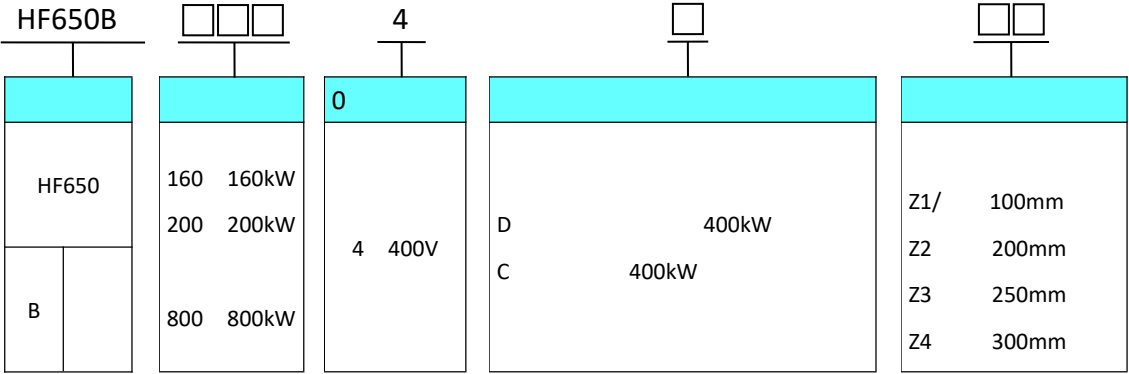
PE

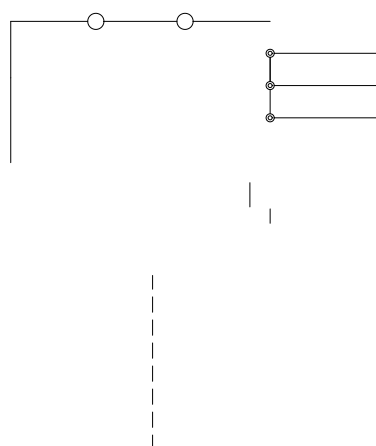






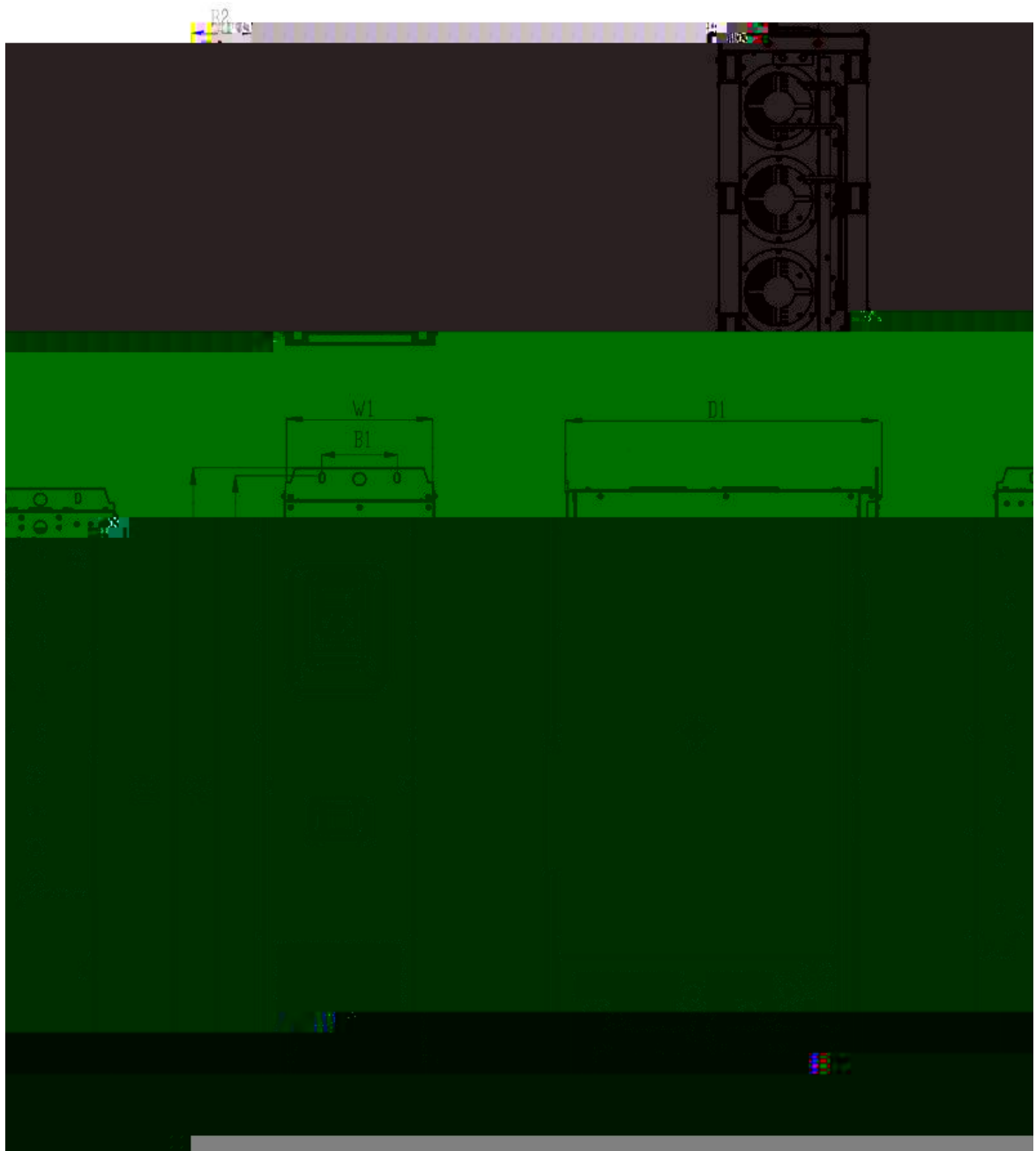


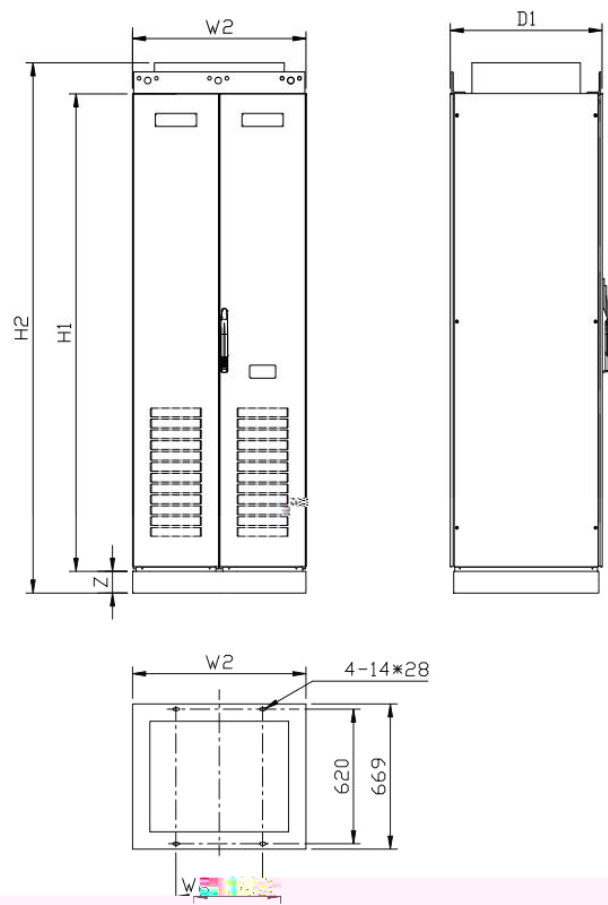


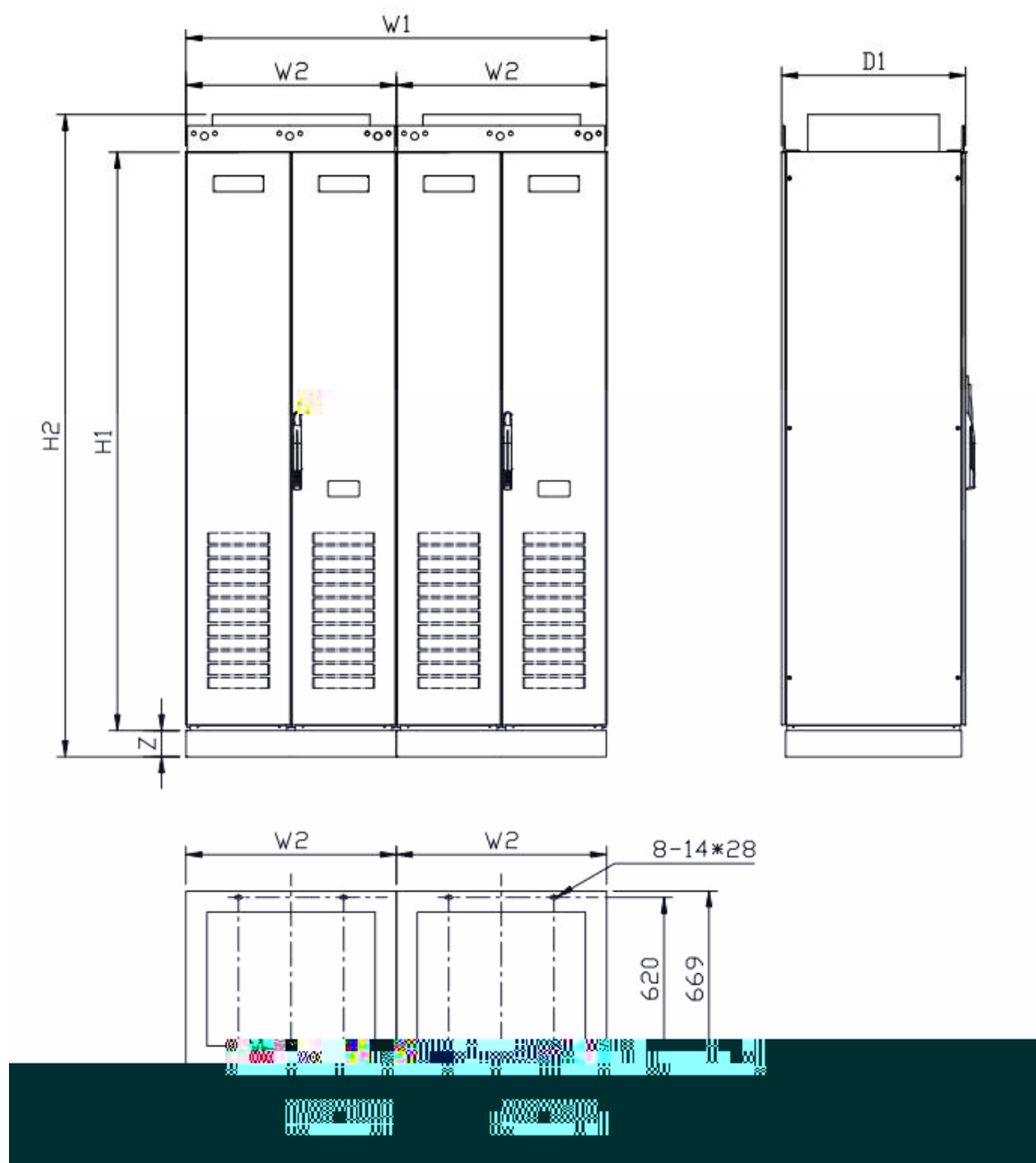


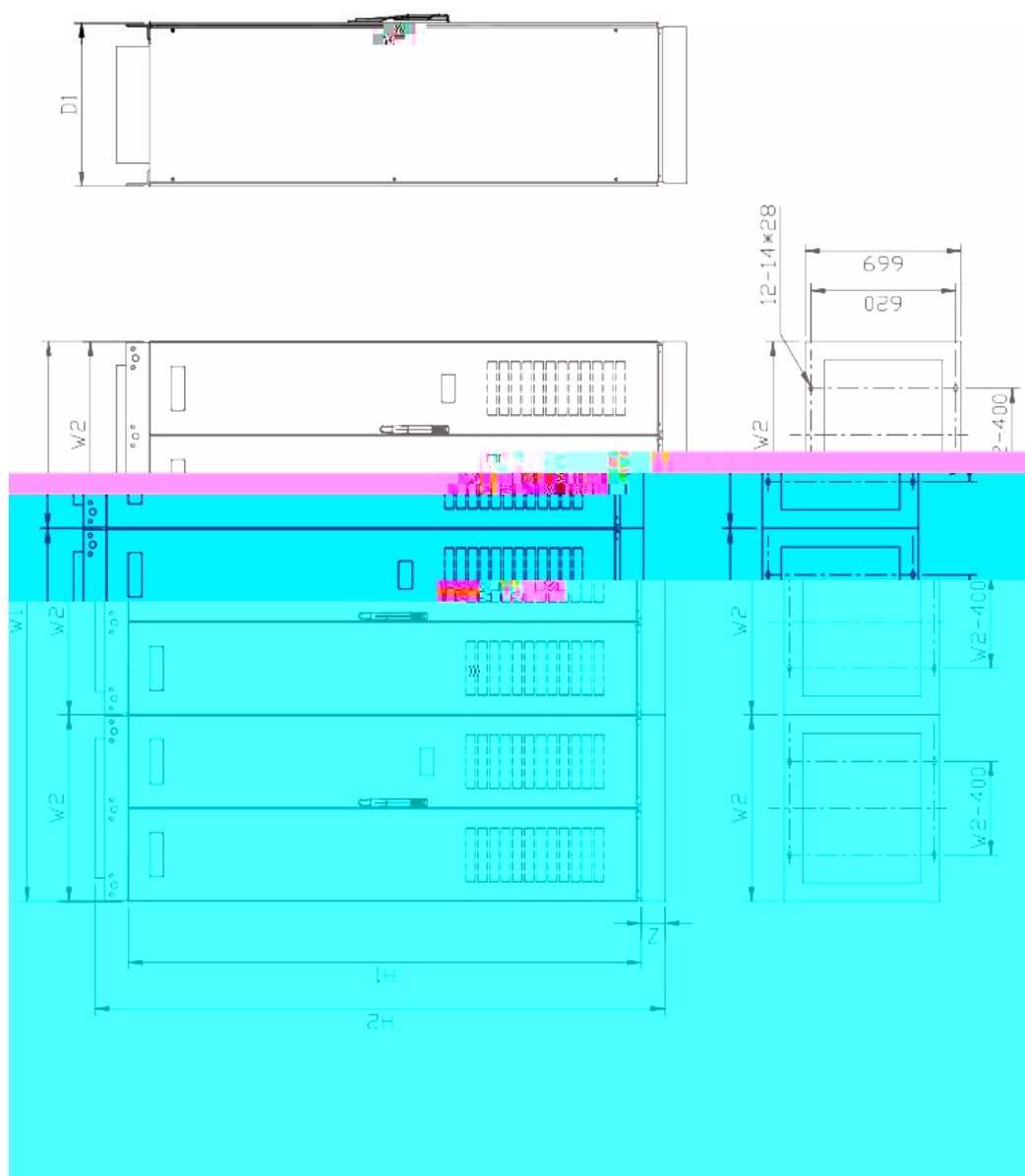
Ã
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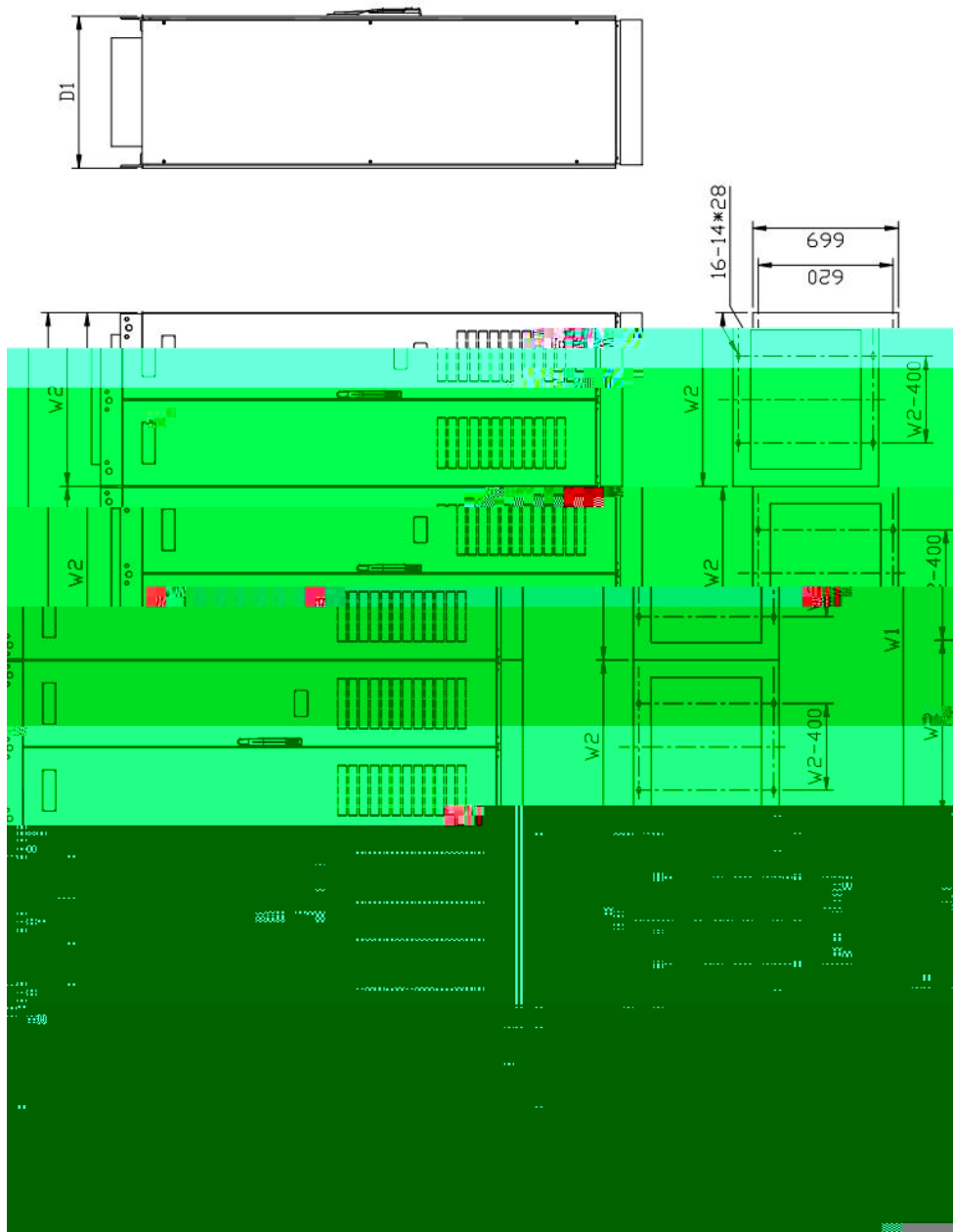
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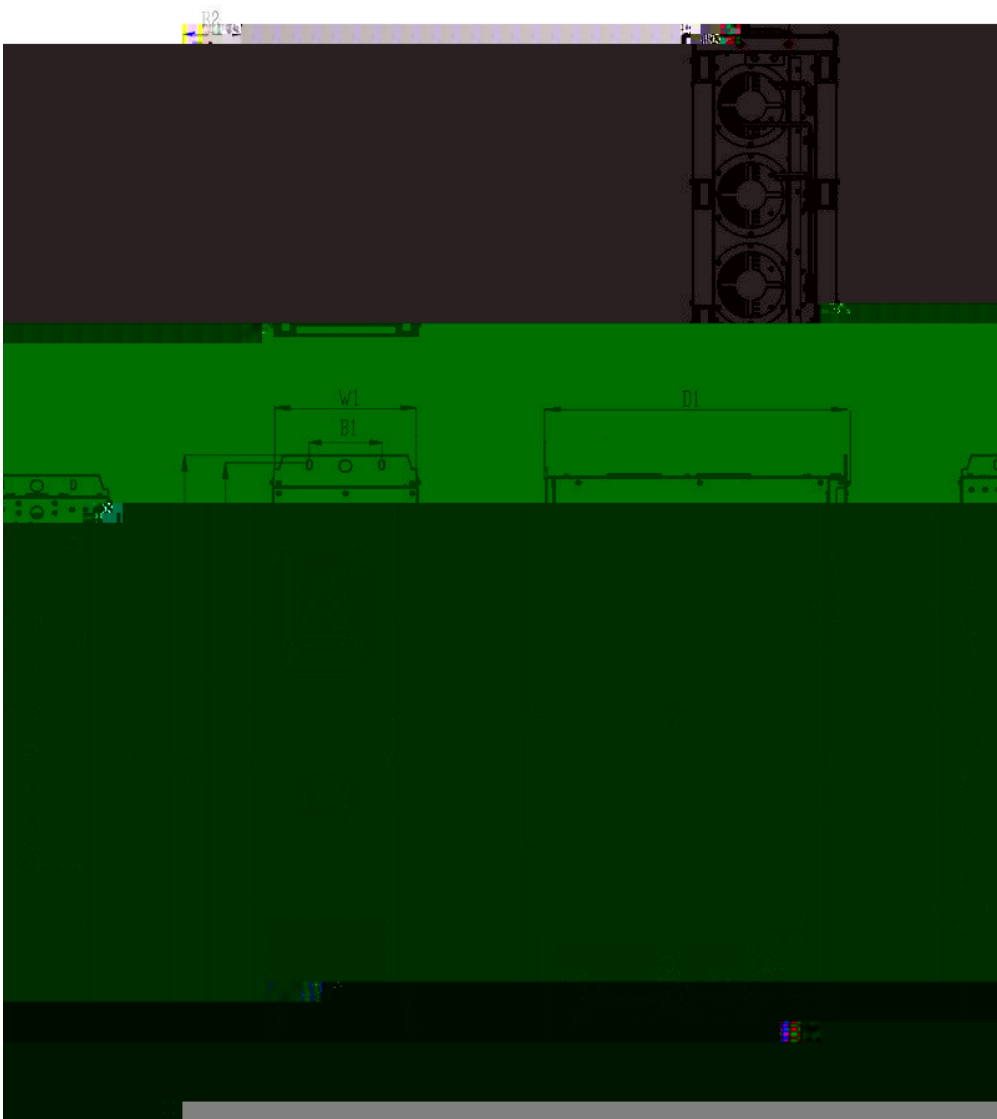


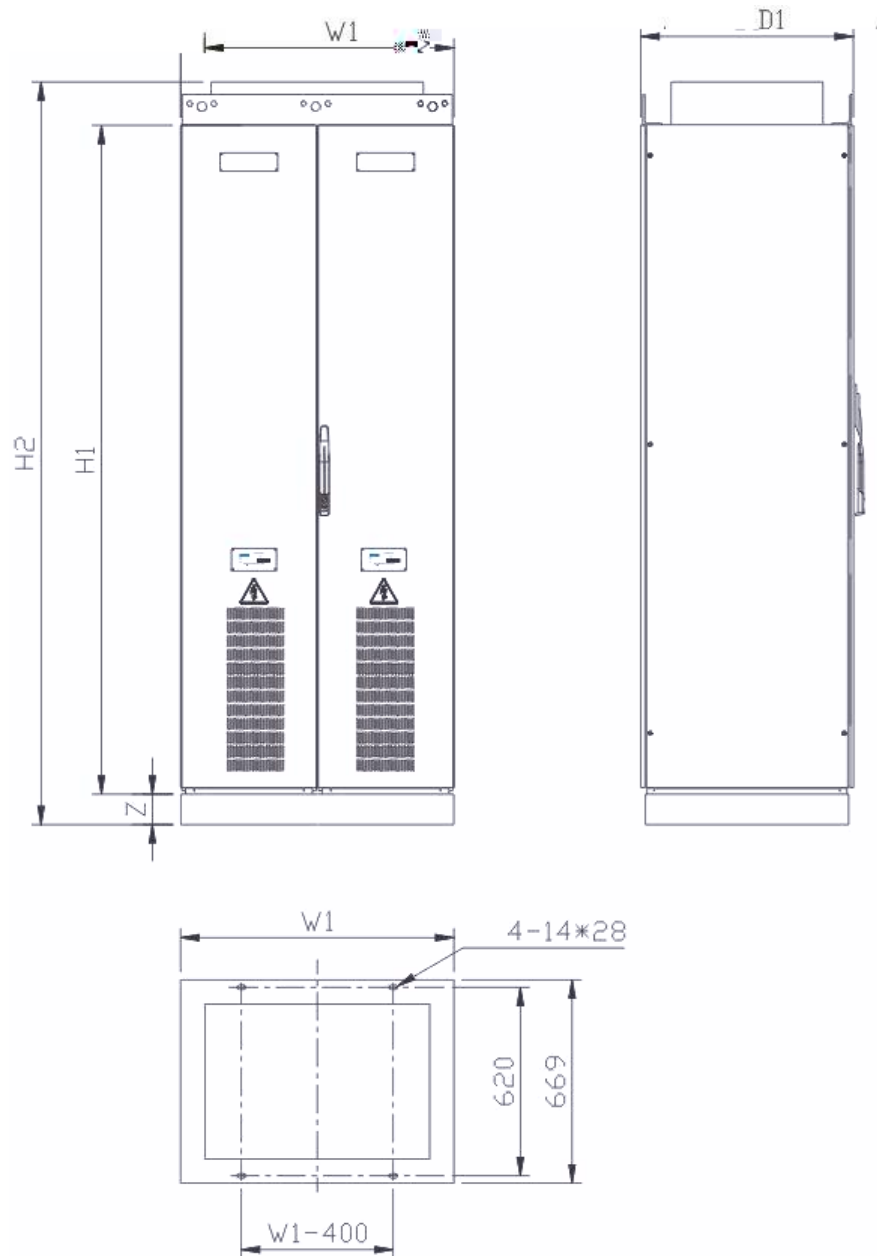






GUIDE





LCD

ENTER

F1

RUN



F2

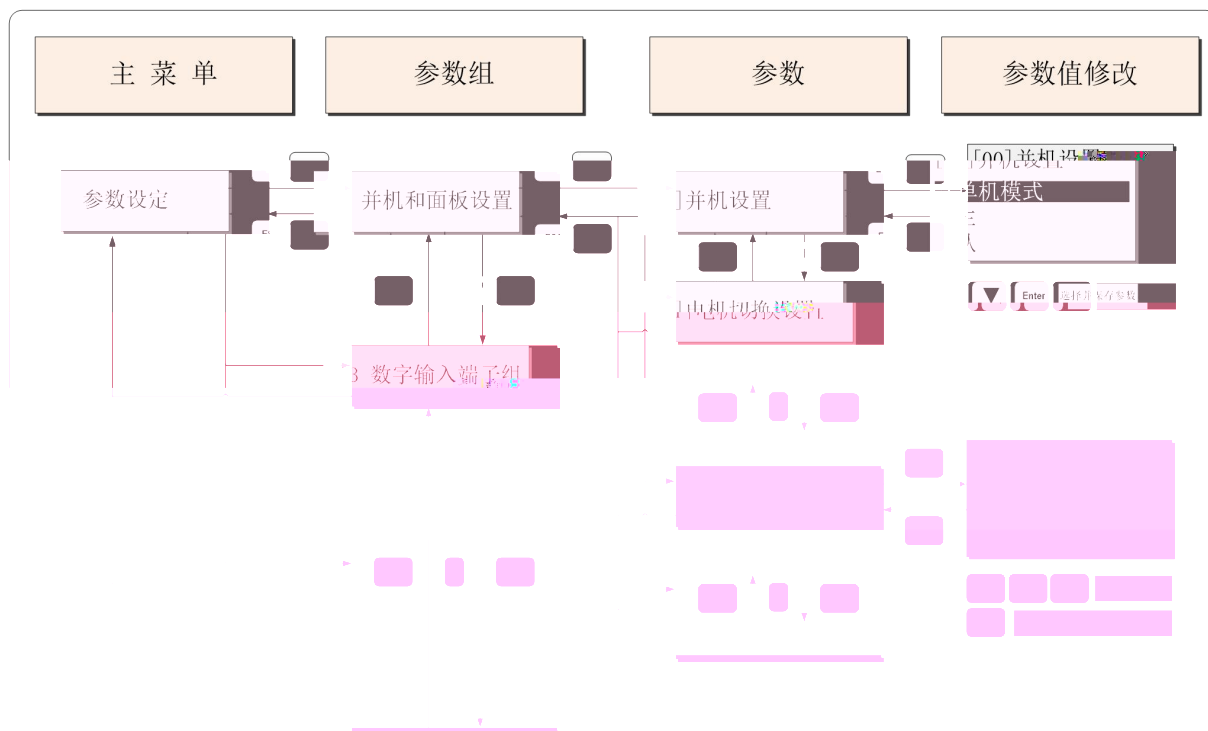
LOCAL/REMOTE

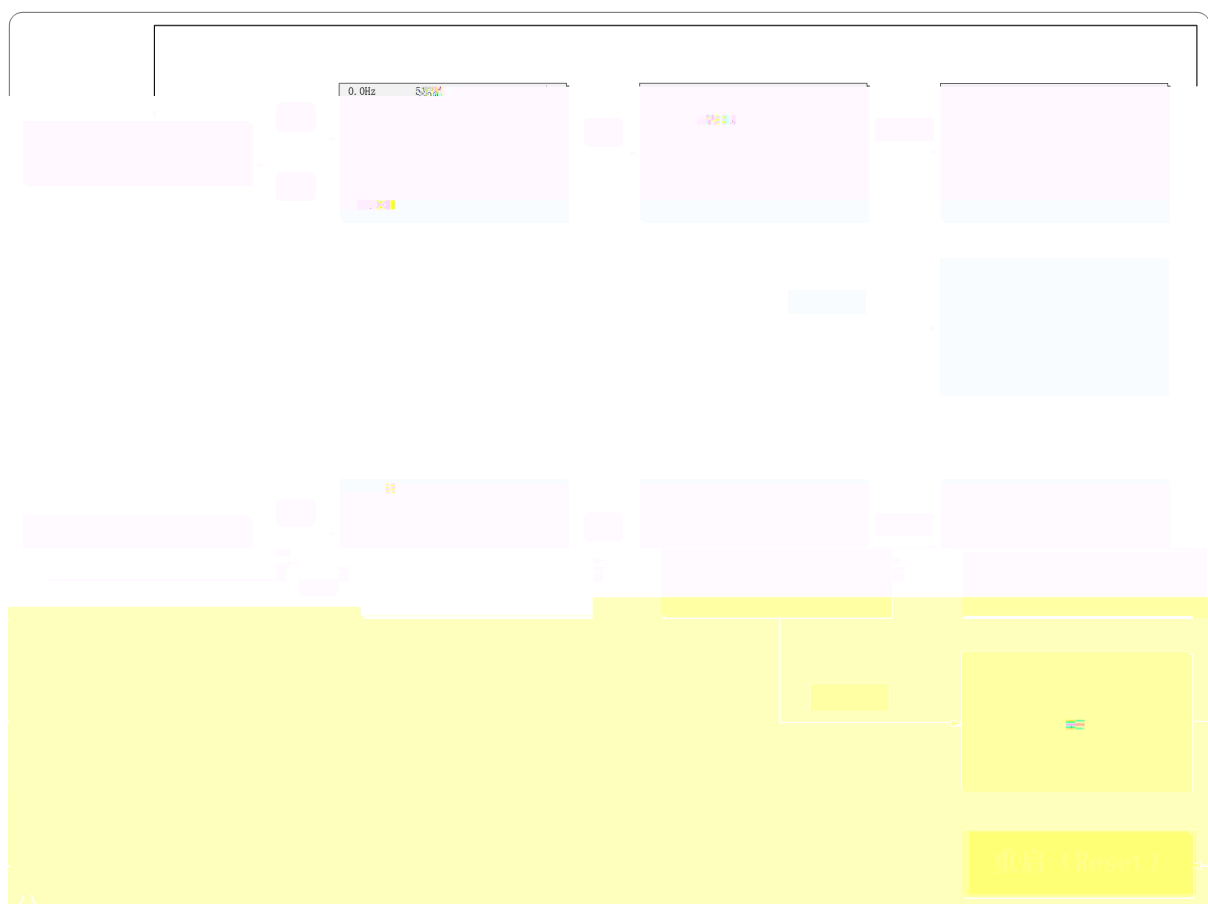
STOP

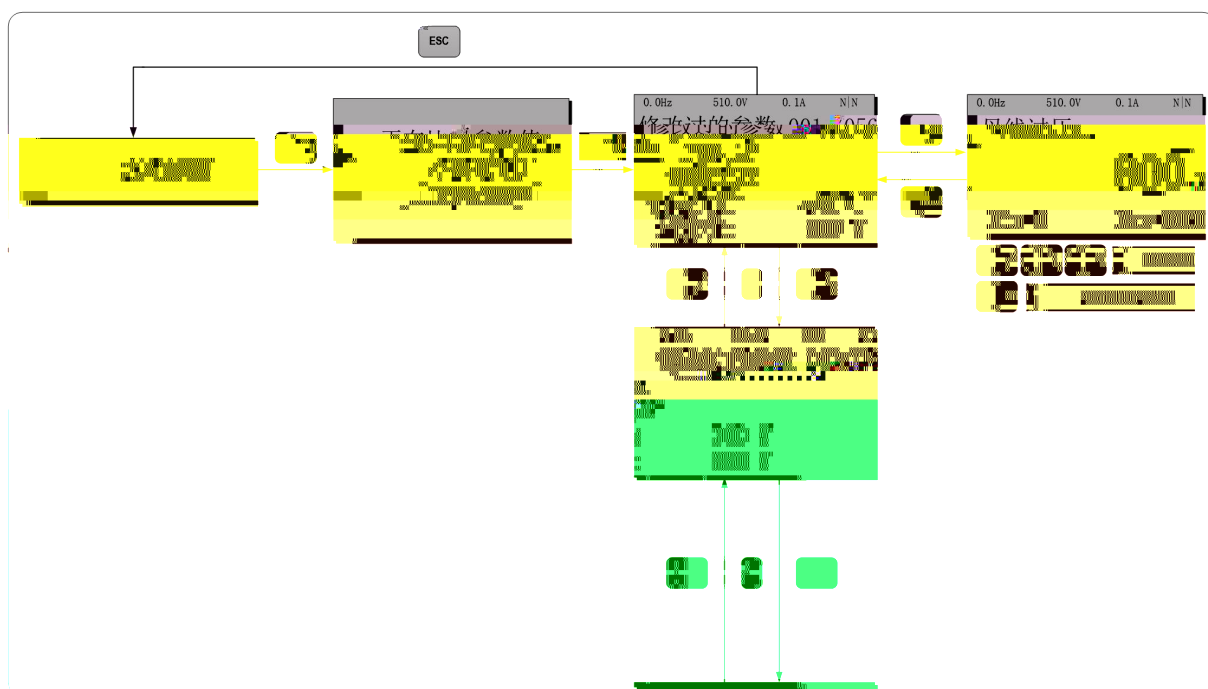
RUN
LOCAL
FAULT
FAULT

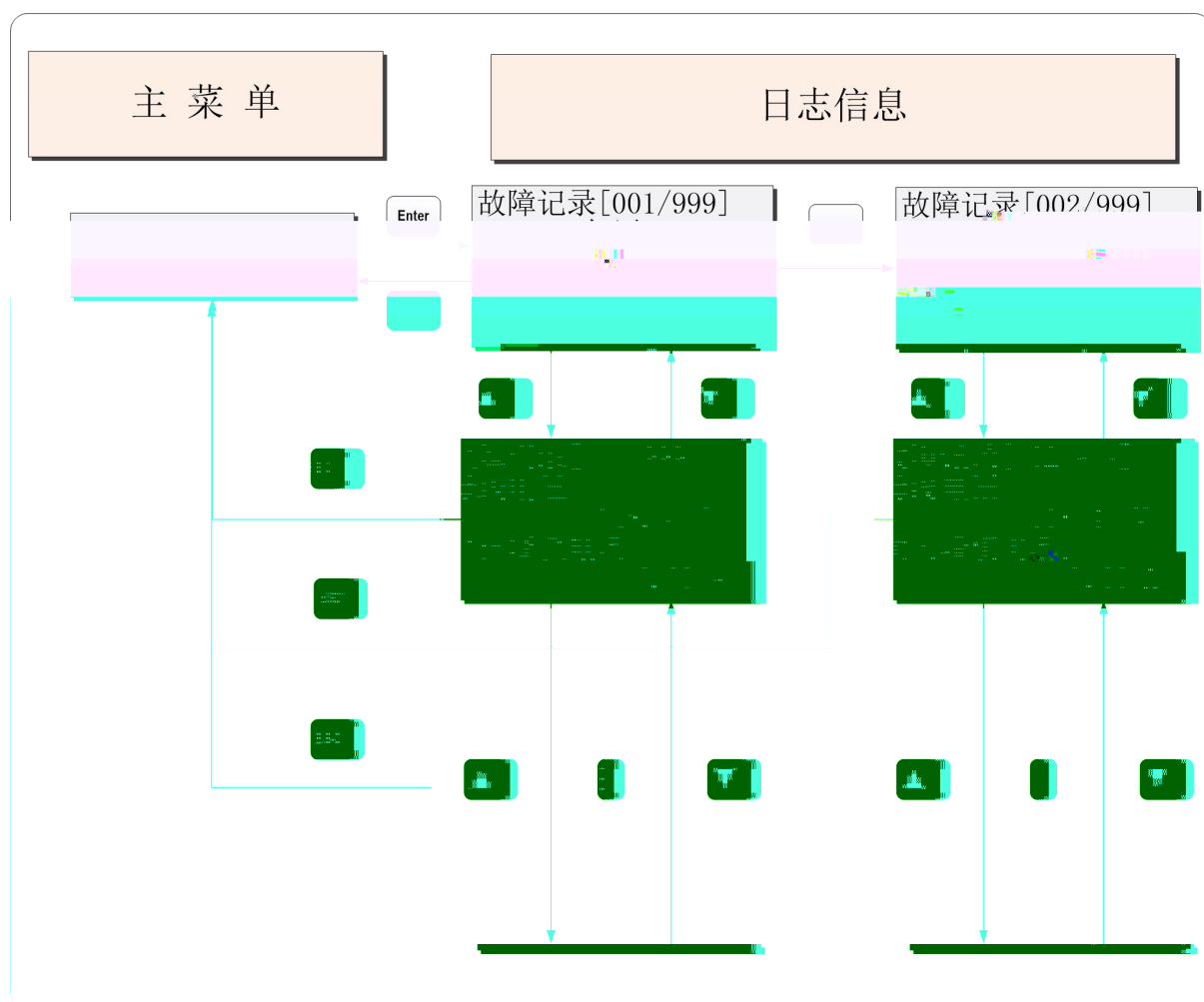


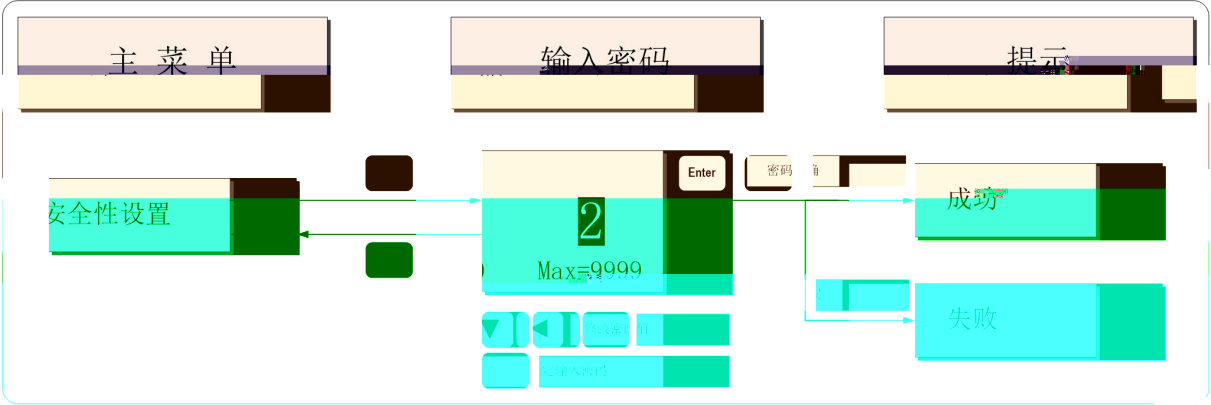
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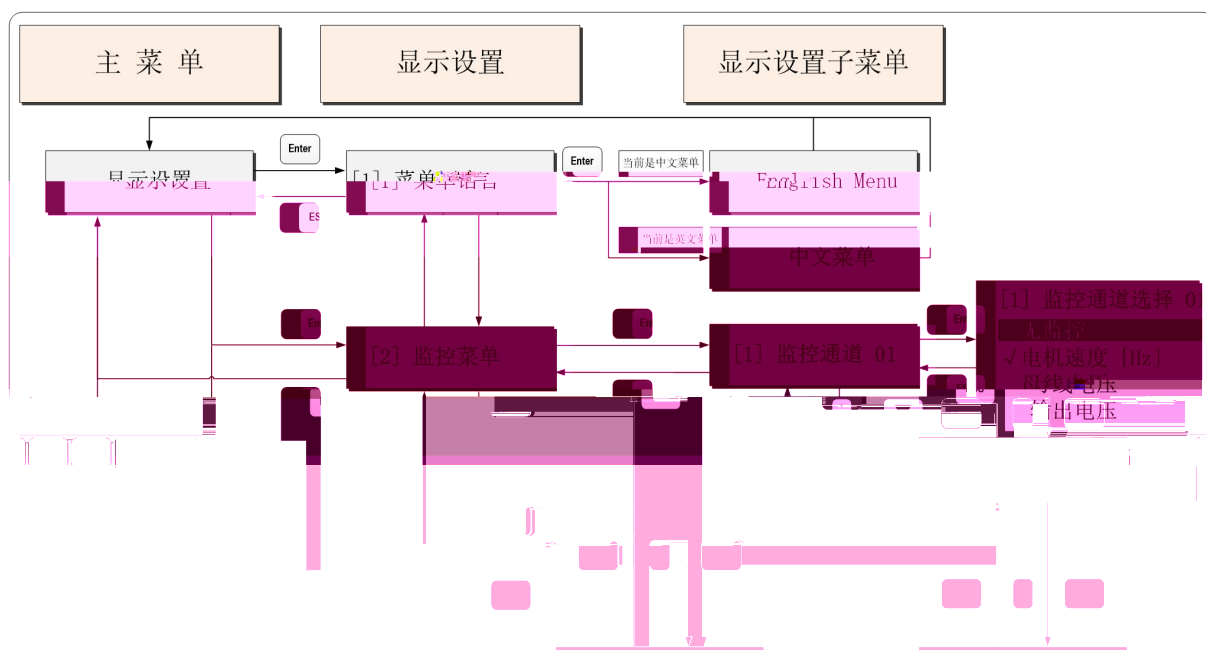


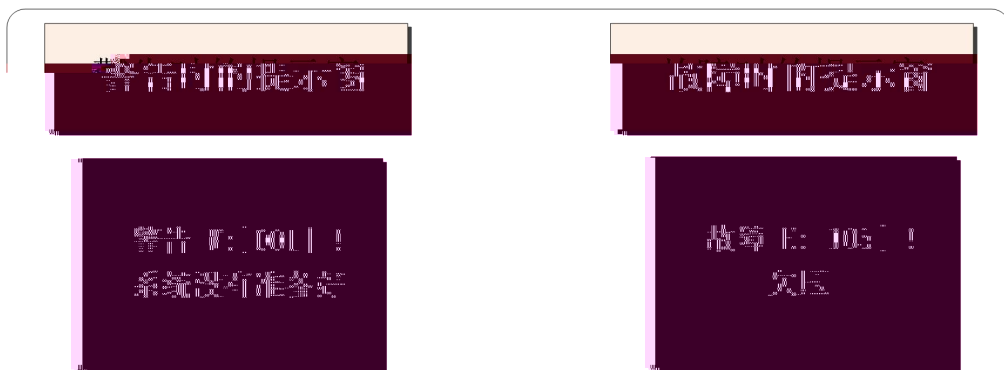












运行

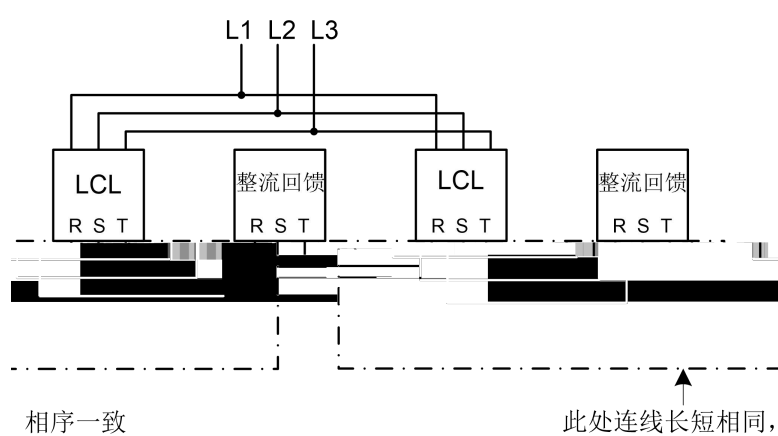
停止

运行

停止

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停止



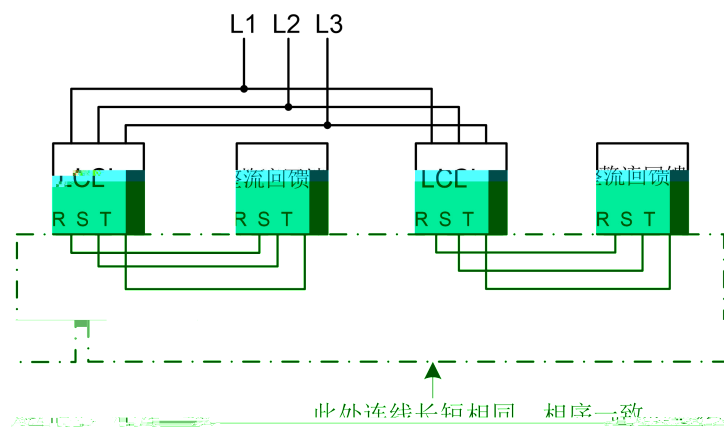




Figure 1 consists of two diagrams, (a) and (b), illustrating optical interconnection structures. Diagram (a) shows a 'master' board at the top with multiple optical ports. Each port is connected by a fiber optic cable to a corresponding port on a 'slave' board below. Diagram (b) shows a 'master' board at the top connected to a central hub or switch. From this hub, multiple fiber optic cables connect to multiple 'slave' boards below.

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运行

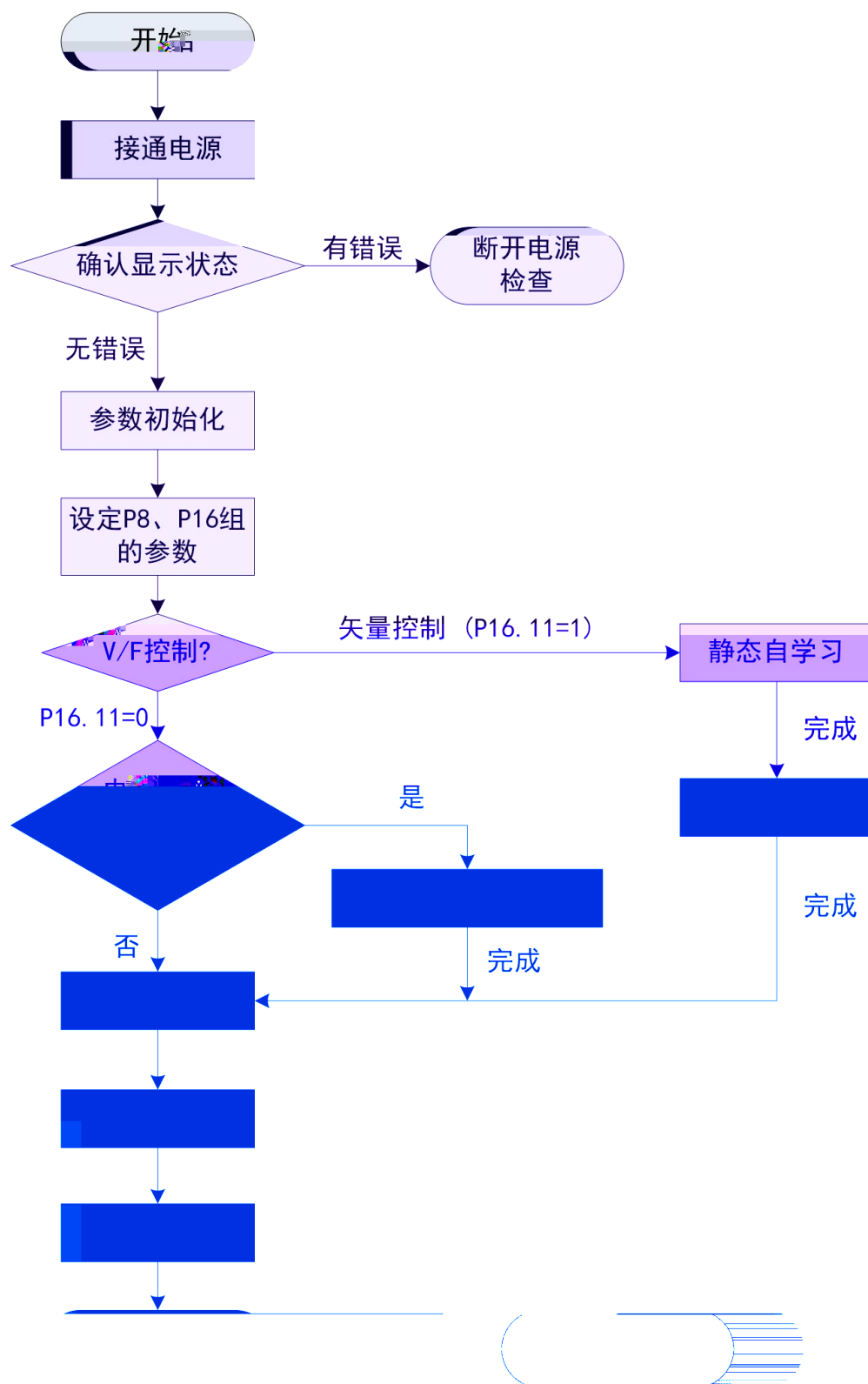
停止

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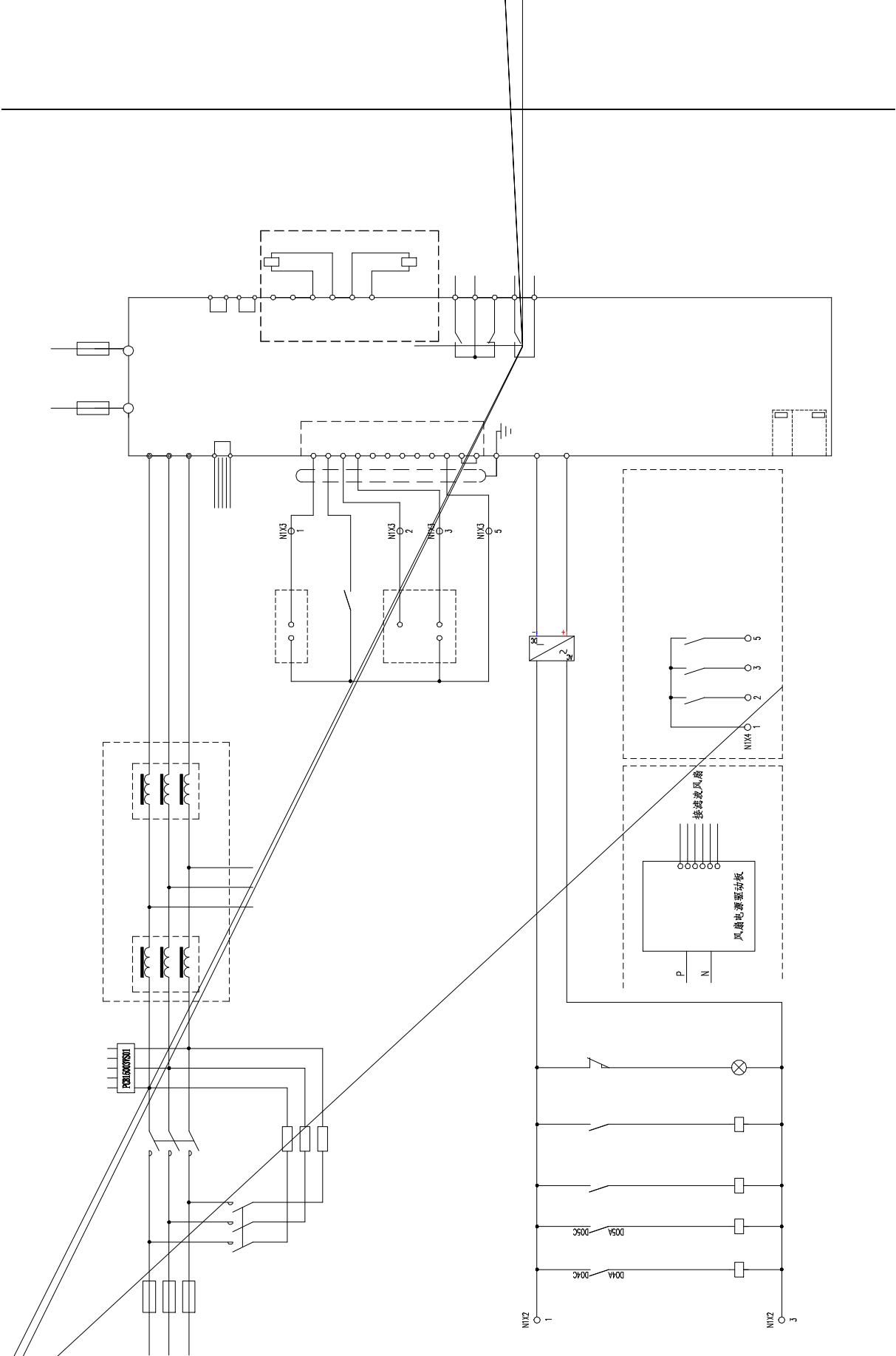
停止

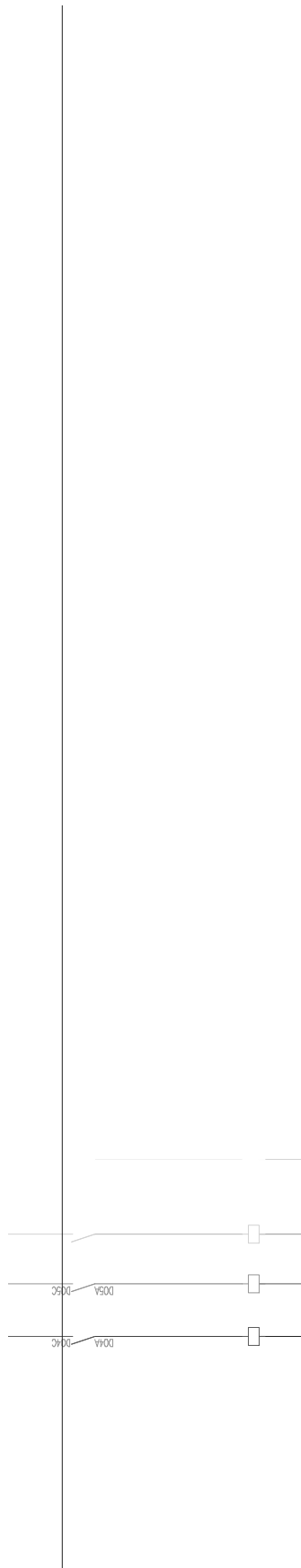


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GUIDE

